



PATENT

Atty. Dkt. No. SOEI/0016

1765

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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JAN 06 2004

TC 1700

In re Application of:  
Tamitani, et al.

Serial No.: 10/030,525

Confirmation No.: 5906

Filed: May 2, 2000

For: Semiconductor Device And  
Method Of Manufacturing A  
Semiconductor DeviceMAIL STOP NON-FEE AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

§§§§§ Group Art Unit: 1765

§§§§§ Examiner: W. A. Powell

CERTIFICATE OF MAILING  
37 CFR 1.8

I hereby certify that this correspondence is being deposited on December 29, 2003, with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

December 29, 2003

Signature

## RESPONSE TO OFFICE ACTION DATED OCTOBER 2, 2003

In response to the Office Action dated October 2, 2003, having a shortened statutory period for response set to expire on January 2, 2004, please enter this response and reconsider the claims pending in the application for reasons discussed below. Although Applicant believes that no additional fees are due in connection with this response, the Commissioner is hereby authorized to charge counsel's Deposit Account No. 20-0782/ SOEI/0016/KMT, for any fees, including extension of time fees or excess claim fees, required to make this response timely and acceptable to the Office.

**Amendments to the Specification** begin on page 2 of this paper. **Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper. **Remarks/Arguments** begin on page 8 of this paper.